



04CD 07-27-01

#3

PATENT
0152-0572P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: K. KISHIMOTO et al. Conf.: UNASSIGNED
Appl. No.: 09/902,746 Group: UNASSIGNED
Filed: July 12, 2001 Examiner: UNASSIGNED
For: SEMICONDUCTOR MODULE & PRODUCING METHOD
THEREFOR

LETTER

Assistant Commissioner for Patents
Washington, DC 20231

August 13, 2001

Sir:

Under the provisions of 35 U.S.C. § 119 and 37 C.F.R. § 1.55(a), the applicant(s) hereby claim(s) the right of priority based on the following application(s):

<u>Country</u>	<u>Application No.</u>	<u>Filed</u>
JAPAN	2000-212068	July 12, 2000
JAPAN	2000-343688	November 10, 2000
JAPAN	2001-074590	March 15, 2001

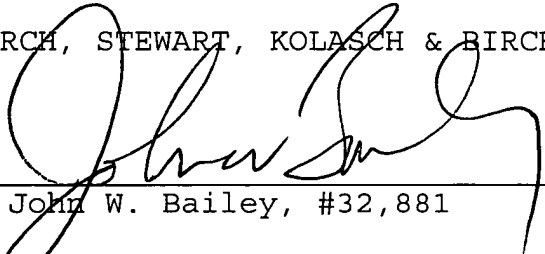
A certified copy of the above-noted application(s) is(are) attached hereto.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fee required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By


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Attachment